

**S/N 09/825,138**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Doron Drusinsky	Examiner:	Dwin M Craig
Serial No.:	09/825,138	Group Art Unit:	2123
Filed:	April 2, 2001	Docket:	1020.P16988
Title:	IMPLEMENTING A FINITE STATE MACHINE USING CONCURRENT FINITE STATE MACHINES WITH DELAYED COMMUNICATIONS AND NO SHARED CONTROL SIGNALS		

**REVOCATION AND POWER OF ATTORNEY**  
**CERTIFICATE UNDER 37 CFR § 3.73(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith:

**CUSTOMER NUMBER: 57035**

**CERTIFICATE UNDER 37 CFR § 3.73(b)**

Intel Corporation hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor(s) filed 06/19/2003 and recorded on Reel 013747, Frames 0257 - 0261. To the best of my knowledge and belief, title is in Intel Corporation, the assignee.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Pam J. Matlock, am empowered to sign this certificate on behalf of Intel Corporation, the assignee.

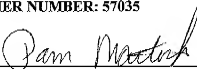
I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

**Kacvinsky LLC**  
**C/o Intellevate**  
**CUSTOMER NUMBER: 57035**

Date 2/20/08

By



Pam J. Matlock  
Intel Corporation